

GOVERNMENT OF INDIA  
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY  
**RAJYA SABHA**  
**UNSTARRED QUESTION NO. 715**  
TO BE ANSWERED ON: 25.07.2025

**DEVELOPMENT OF SEMICONDUCTOR MANUFACTURING ECOSYSTEM**

**715. SHRI KARTIKEYA SHARMA:**

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:

- (a) the progress made in development of semiconductor manufacturing ecosystem in the country;
- (b) the details of the recently signed contracts and the proposed date of initiation of these projects along with total value and job creation;
- (c) the outcome so far with regard to recently launched Production Linked Incentive (PLI) scheme for electronics components manufacturing; and
- (d) the steps being taken by the Ministry to develop skilled manpower for the semiconductor and other electronic components manufacturing ecosystem?

**ANSWER**

MINISTER OF STATE FOR ELECTRONICS AND INFORMATION TECHNOLOGY  
(SHRI JITIN PRASADA)

(a) to (d): Government has approved Semicon India programme with a total outlay of Rs. 76,000 crore for the development of semiconductor and display manufacturing ecosystem in the country. It provides:

1. Fiscal support of 50% of the project cost on *pari-passu* basis for setting up of Silicon Complementary Metal-Oxide-Semiconductor (CMOS) based Semiconductor Fabs in India.
2. Fiscal support of 50% of Project Cost on *pari-passu* basis for setting up of Display Fabs in India.
3. Fiscal support of 50% of the Capital Expenditure on *pari-passu* basis for setting up of Compound Semiconductors / Silicon Photonics (SiPh) / Sensors (including Micro-Electro-Mechanical Systems) Fab/ Discrete Semiconductor Fab and Semiconductor Assembly, Testing, Marking and Packaging (ATMP) / Outsourced Semiconductor Assembly and Test (OSAT) facilities in India.
4. Product Design Linked Incentive of up to 50% of the eligible expenditure subject to a ceiling of ₹15 Crore per application and also “Deployment Linked Incentive” of 6% to 4% of net sales turnover over 5 years subject to a ceiling of ₹30 Crore per application for incentivising chip design.

**Semiconductor manufacturing in India**

- Six Semiconductor Projects approved under Semicon India with ₹1.55 Lakh cr investment
- The projects are expected to generate over 27,000 direct jobs
- The approved projects are currently at various stages of implementation

### **Electronics Components Manufacturing Scheme (ECMS)**

- ECMS was launched on 8th April 2025, with a budgetary outlay of ₹22,919 crore.
- Aims to attract investments across the component, sub-assembly, and capital equipment
- It is a **pan India** scheme. The applications for the scheme are **currently being accepted** on the portal.
- Details of the scheme are available on [ecms.meity.gov.in](https://ecms.meity.gov.in).

India is home to nearly 20% of the world's chip design engineers. Building on this strong talent base, the Government is facilitating the establishment of a semiconductor manufacturing ecosystem in India:

- New curriculum by All India Council for Technical Education (AICTE) for VLSI Design & Technology, Integrated Circuit (IC) manufacturing
- Developing 85,000 skilled manpower in semiconductor design sector & providing EDA tools to design semiconductor chips
  - 45,000+ students from 100 institutions enrolled so far
- Skilled Manpower Advanced Research and Training (SMART) Lab at NIELIT Calicut to train 1 lakh engineers nation-wide with 44,000+ engineers already trained
- Under National Skill Development Mission, qualification standards have been developed for the entire value chain of the semiconductor industry
- Collaboration with industry & universities such as Lam Research, IBM & Purdue University for research, education and workforce development in semiconductors

\*\*\*\*\*

